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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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### Complete if Known

Application / Conf. No.	10/783,216 / 6678
Filing Date	February 20, 2004
First Named Inventor	Tim Tuan
Art Unit	2825
Examiner Name	Nelson G. Lam GARBOWSKI
Attorney Docket Number	X-1462-1P US

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### U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number		Publication Date MM-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code (if known)			
✓		US- 5,362,989		11-1994	Hennedy	
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		US- 6,936,917		08-2005	Lopata et al.	

### FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document		Publication Date MM-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T *
		Country Code	Number - Kind Code (if known)				

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		Number	Kind Code (If known)			
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Sheet 3 of 4	Attorney Docket Number	X-1462-1P US	

OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
W		ALLEN/HOLBERG; Chapter 10; "Bandgap Voltage Reference"; April 12, 2000; downloaded on January 3, 2006 from www.ece.utexas.edu/~holberg/lecture_notes/bandgap.pdf; pp. 1-5.	
		MICROCHIP TECHNOLOGY INC.; "Micropower Voltage Supervisors"; MCP102/103/121/131; Copyright 2005; downloaded on January 3, 2006 from www1.microchip.com/downloads/en/DeviceDoc/21906b.pdf; pp. 1-28	
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		11/325,888 filed 01/04/06, TUAN, TIM, entitled "Programmable Low Power Modes For Embedded Memory Blocks", Xilinx Inc., San Jose, CA	

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**OTHER - NON PATENT LITERATURE DOCUMENTS**

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W		11/326,542 filed 01/04/06, JACOBSON, NEIL G., et al., entitled "Method and Mechanism for Controlling Power Consumption of an Integrated Circuit", Xilinx, Inc., San Jose, CA	
		11/329,646 filed 01/11/06, JENKINS, JESSE H. IV, entitled "Programmable Detection of Power Failure in an Integrated Circuit", Xilinx, Inc., San Jose, CA	
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